

KLT-N6MF-IMX274 V1.0**SONY IMX274 MIPI Interfaccia Messa a fuoco fissa 8.3MP M12 Modulo telecamera**

Modulo telecamera n.	KLT-N6MF-IMX274 V1.0
Sensore d'immagine	IMX274
EFL	3.60 mm
F.NO	2.0
Pixel	3840 x 2160
Vista ad angolo	140°
Tipo di lente	1/2.5 pollice
Dimensioni dell'obiettivo	13.65 x 13.65 x 22.52 mm
Dimensione del modulo	40.00 x 22.00 mm
Tipo di modulo	Messa a fuoco fissa
Interfaccia	MIPI

Connettore di accoppiamento Codice. DF30FC-30DS-0.4V

Connettore di accoppiamento sulla scheda principale. Venduto separatamente.

IMX274LQC

Diagonal 7.20 mm (Type 1/2.5) Approx. 8.51M-Effective Pixel
Color CMOS Image Sensor

16:9 Aspect Ratio CMOS Image Sensor Capable of 4K (3840 × 2160) Output for Industrial Applications

Sony has commercialized the "IMX274LQC" Type 1/2.5 (16:9) back-illuminated CMOS image sensor with approximately 8.51M effective pixels for the expanding 4K market.

The IMX274LQC is capable of 4K (3840 × 2160) output at 60 frames/s in ADC 10-bit mode. In addition, the DOL (Digital Overlap)-type HDR (High Dynamic Range) function is

supported at 30 frames/s, realizing 4K video imaging with a wide dynamic range. The IMX274LQC has lower power consumption and is smaller than the existing 4K support CMOS image sensor (IMX172LQT), and the interface supports Sub-LVDS and MIPI CSI-2, enabling use in security camera and industrial applications.

- High-speed video imaging function
- Versatile interface
- DOL-HDR function
- Compact device size

Exmor R

*Exmor R is a trademark of Sony Corporation. The Exmor R is a Sony's CMOS image sensor with significantly enhanced imaging characteristics including sensitivity and low noise by changing fundamental structure of Exmor™ pixel adopted column parallel A/D converter to back-illuminated type.

STARVIS

*STARVIS is a trademark of Sony Corporation. The STARVIS is back-illuminated pixel technology for CMOS image sensors for surveillance camera applications. It features a sensitivity of 2000 mV or more per 1 μm² (color product, when imaging with a 706 cd/m² light source, F5.6 in 1 s accumulation equivalent), and realizes high picture quality in the visible-light and near infrared light regions.

High-speed video imaging function

The IMX274LQC realizes 4K (3840 × 2160) all-pixel scan at 30 frames/s in ADC 12-bit mode, and at the high frame rate output of 60 frames/s in ADC 10-bit mode, making it the ideal CMOS image sensor for expanding 4K market applications. The IMX274LQC also realizes Full HD (1920 × 1080) output at 60 frames/s in ADC 12-bit mode (mode 1) and 120 frames/s

in ADC 10-bit mode (mode 2), enabling high-speed video imaging. In addition, various other readout methods are also supported, and diverse drive modes can be selected (Table-3).

Use of a lower frame rate makes it possible to reduce power consumption.

Versatile interface

The IMX274LQC is equipped with two types of output interface (Sub-LVDS, MIPI CSI-2) to meet diverse needs. Both interfaces are capable of 4K 60 frames/s (ADC 10-bit) output, so the

interface can be selected in accordance with the DSP and system used (Table-1).

DOL-HDR function

The IMX274LQC is Sony's first CMOS image sensor to support a DOL-type HDR function for a 4K angle of view. This makes it possible to shoot 4K video with an expanded dynamic

range. The modes that support the DOL function are 4K (3840 × 2160) ADC 10-bit 30 frames/s and Full HD (1920 × 1080) ADC 10-bit 60 frames/s (Table-3).

Compact device size

The IMX274LQC realizes a compact package size of 10.70 mm (H) × 8.50 mm (V) × 1.62 mm (t). This reduced camera

size expands the range of potential security camera and industrial applications.

<Table 1> Device Structure

Item		IMX274LQC
Output image size		Diagonal 7.20 mm (Type 1 / 2.5) aspect ratio 16:9
Number of effective pixels		3864 (H) × 2202 (V) approx. 8.51M pixels
Unit cell size		1.62 μm (H) × 1.62 μm (V)
Optical blacks	Horizontal	Front: 0 pixels, rear: 0 pixels
	Vertical	Front: 16 pixels, rear: 0 pixels
Input drive frequency		12 MHz / 24 MHz / 36 MHz / 72 MHz (Sub-LVDS) 6 MHz / 12 MHz / 18 MHz / 24 MHz (MIPI CSI-2)
Interface		Sub-LVDS (576 Mbps / ch, Max.10 ch) *1 MIPI CSI-2 (1.440 Gbps / Lane) *1
Package		92-pin LGA
Supply voltage V _{DD} (Typ.)		2.8 V / 1.8 V / 1.2 V

*1 Sensor slave mode when using Sub-LVDS and sensor master mode when using MIPI.

<Table 2> Image Sensor Characteristics

Item	Value	Remarks
Sensitivity (F5.6)	Typ. 237 mV	1/30s accumulation
Saturation signal	Min. 630 mV	T _j = 60 °C

<Table 3> Basic Drive Mode

Drive mode	Recommended number of recording pixels	Frame rate [frame/s]	ADC[bit]
All-pixel scan (12 bit)	3840 (H) × 2160 (V)	29.97	12
All-pixel scan (10 bit)	3840 (H) × 2160 (V)	59.94	10
Mode 1*2 (12 bit)	1920 (H) × 1080 (V)	59.94	12
Mode 2*2 (10 bit)	1920 (H) × 1080 (V)	119.88	10
Mode 3*2 (10 bit)	1920 (H) × 1080 (V)	29.97	10
Mode 4*2	1280 (H) × 720 (V)	119.88	10
Mode 5*2	1280 (H) × 540 (V)	239.76	10

Drive mode	Recommended number of recording pixels	Frame rate [frame/s]	ADC[bit]
All-pixel scan (10 bit) DOL*1	3840 (H) × 2160 (V)	29.97	10
Mode 6 (10 bit) DOL*1*2	1920 (H) × 1080 (V)	59.94	10

*1 There are restrictions on the storage time setting values when using DOL.

*2 With vertical addition

*Sony reserves the right to change products and specifications without prior notice.